EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	11	("5731728"|"5736893"|"6107851"|"6229366"|"6249876").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 07:50
L16	2	((electromagnetic adj interference adj cancellation)) and ((control adj signal) with counter) and ((voltage adj control) with oscillator)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 09:07
L17	4	(((electromagnetic adj interference) or EMI) with (cancellation or reduction)) and ((control adj signal) with counter) and ((voltage adj control) with oscillator)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 09:12
L18	36	(((electromagnetic adj interference) or EMI) with (cancellation or reduction)) and ((control) with counter) and ((voltage) with oscillator)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 11:25
L19	986	((control) with counter with (opposite or negative or invert$3)) and ((voltage) with oscillator)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 12:22
L20	32	((control) with counter with (opposite or negative or invert$3) with alternate$2) and ((voltage) with oscillator)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 11:28
L21	1	((control) with counter with (opposite or negative or invert$3) with alternate$2) and ((voltage) with oscillator) and emi	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 11:29

EAST Search History

L22	1	((control) with counter with (opposite or negative or invert$3) with alternate$2 with (n adj bit)) and ((voltage) with oscillator) and emi	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 11:29
L23	2	((control) with counter with (opposite or negative or invert$3) with alternate$2 with (n adj bit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 11:51
L24	2	"5612956".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 11:39
L25	4	"4408283".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 12:59
L26	4	"4,408,283".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 11:41
L27	2	"6,317,476".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 11:42
L28	2	"6,242,965".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 11:43

EAST Search History

L29	2	"4,528,662".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 11:43
L30	173	((control) with counter with (opposite or negative or invert$3)) with alternate$2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 11:51
L31	11	(counter with reverse with bit) with alternate$2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 11:54
L32	12	(counter with reverse with cycle) with alternate$2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 11:54
L33	1	(counter with reverse with cycle) with alternate$2 and EMI	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 12:16
L34	3771	375/346	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 12:21
L35	285	375/287	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 12:21

EAST Search History

L36	2349	455/296	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 12:21
L37	1	19 and 34	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 12:22
L38	0	19 and 35	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 13:06
L39	1	19 and 36	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 12:22
L40	1002	Balakrishnan.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 12:36
L41	42	Balakrishnan.in. and EMI	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 12:36
L42	2371	emi.ti.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 12:59

EAST Search History

L43	122	emi with (reduction or cancellation or reduce or cancel).ti.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 13:00
L44	4	emi with (reduction or cancellation or reduce or cancel).ti. and counter and oscillator	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 13:01
L45	10	emi with (reduc$4 or cancellat$3). ti. and counter and oscillator	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 13:06
L46	1204	713/501	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 13:06
L47	1	19 and 46	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 13:06
S1	1	"10/396118"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/27 08:40
S2	1	10/665080	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/14 13:45

Web Results **1** - **10** of about **65** for **control counter opposite alternately "n bit" EMI**. (**0.50** seconds)

EMI cancellation method and system - Patent 20040096020
In an **EMI** canceller, a **control** signal generation unit includes a **counter**, ... the method comprising: **alternately** outputting an **n-bit** signal and a reverse ...
www.freepatentsonline.com/20040096020.html - 35k - Cached - Similar pages

Phase-locked loop circuit and frequency modulation method using ...
Such variation of the oscillated frequency suppresses the **EMI** problems. ... The **n-bit** down **counter** 11, the multiplexers 12, the **control** signal generating ...
www.freepatentsonline.com/20020017933.html - 37k - Cached - Similar pages
[More results from www.freepatentsonline.com]

Reformatting of variable rate data for fixed rate communication ...
Alternating binary ones are encoded with **opposite** polarities, such that a positive ...
Previous Patent (Mobile radio with transmit command **control** an. ...
www.wikipatents.com/5612956.html - 132k - Cached - Similar pages

[PDF] CR16MCS9 CompactRISC 16-Bit Reprogrammable Microcontroller
File Format: PDF/Adobe Acrobat
at the clock rate selected for the **counter**. When an underflow. occurs, the timer register is reloaded **alternately** from the. TnCRA and TnCRB register, ...
www.national.com/appinfo/compactrisc/pdf/cb_datasheet.pdf - Similar pages

Method and apparatus for receiving high speed signals with low ...
2, operates on **opposite** clock edges senses and latches odd data values (e.g., ...
Alternately, the bus output drivers 323 and bus receivers 324 of the ...
www.patentstorm.us/patents/6396329-description.html - 236k - Cached - Similar pages

[PDF] MMode odell MTDD--135 135
File Format: PDF/Adobe Acrobat - View as HTML
Precise monitoring and **control** of temperature over a wide range. ... Close transfer line flow valve using full **counter**-clockwise rotation when looking down ...
www.lakeshore.com/pdf_files/Obsolete/MTD135_Manual.pdf - Similar pages

[PDF] MTD Series
File Format: PDF/Adobe Acrobat - View as HTML
EMI is an electromagnetic phenomena which, either directly or indirectly, ... Close the transfer line flow valve using (full **counter**-clockwise rotation when ...
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[PDF] High Accurate Clock Synchronization in Distributed Systems
File Format: PDF/Adobe Acrobat
If two clocks are drifting in **opposite** direction, at a time Δt after they ... 2. a program to **control** and read-out the frequency **counter** from a remote host ...
embsys.technikum-wien.at/staff/horauer/pubs/thesis.pdf - Similar pages

[PDF] Chip Implementation and Measurement
File Format: PDF/Adobe Acrobat
Because **control** signals from frequency loop and phase. loop are combined to **control** VCO, it is possible that their signals drive VCO in the **opposite** ...

www.lib.ncsu.edu/theses/available/etd-02202003-151133/unrestricted/etd.pdf -
Similar pages

[PDF] Introduction to Microcontrollers
File Format: PDF/Adobe Acrobat
To do so, another special register, the program. **counter** (PC), is used to store the address of the next program instruction. The **control** unit loads ...
wwwold.ecs.tuwien.ac.at/lehre/Micro**control**ler/Docs/Lecture/Micro**control**ler.pdf -
Similar pages

Result Page: 1 2 3 4 5 **Next**

Web Results **1** - **10** of about **82** for **control counter opposite alternately "n bit" "voltage control" oscillator**.

EMI cancellation method and system - Patent 20040096020
The **oscillator** receives the voltage of the **voltage control** unit as a high ... That is, the **counter** 110 **alternately** outputs the output signals (Q1-Qn) and ...
www.freepatentsonline.com/20040096020.html - 35k - Cached - Similar pages

Multiplex **control** system having enhanced integrity - Patent 4528662
Mux computer 12 also includes **oscillator** and **counter**/timer circuitry, ... loads typically provide low **voltage control** signals to various **control** elements or ...
www.freepatentsonline.com/4528662.html - 122k - Cached - Similar pages
[More results from www.freepatentsonline.com]

United States Patent: 4408283
oscillator means having an output coupled with said digital circuit means for ... for deriving a **voltage control** signal, and a voltage controlled **oscillator** ...
www.wikipatents.com/patenthtml.php?id=4408283 - 56k - Cached - Similar pages

Phase locked loop - US Patent 5539357
331/17 , Particular error **voltage control** (e.g., intergrating network) 327/159 ... arts for synchronizing a variable local **oscillator** with the phase and/or ...
www.patentstorm.us/patents/5539357-description.html - 71k - Cached - Similar pages

Fractional N-frequency synthesizer and spurious signal cancel ...
If the accumulator 6 has a **n-bit** structure, an overflow occurs when the held ... 91 and NPN type current mirror circuits 92 which are **alternately** combined, ...
www.patentstorm.us/patents/5818303-description.html - 74k - Cached - Similar pages
[More results from www.patentstorm.us]

[PDF] Chip Implementation and Measurement
File Format: PDF/Adobe Acrobat
2.3.3 **Voltage Control Oscillator**. The recovered clock is generated by VCO ... NRZ data is digitally sampled N times producing **N-bit** oversampling data. ...
www.lib.ncsu.edu/theses/available/etd-02202003-151133/unrestricted/etd.pdf - Similar pages

[PDF] Derwent
File Format: PDF/Adobe Acrobat - View as HTML
Novelty: A ring **oscillator control** circuit receives an oscillation ... output voltage and an external reference voltage as a threshold **voltage control** ...
scientific.thomson.com/ts/media/dw/productpdfs/itppdfs/itelosc.pdf - Similar pages

[PDF] Derwent
File Format: PDF/Adobe Acrobat - View as HTML
Detailed Description: The user apparatus (2) includes a **control** part (21), a. storing part (22) and a tamper-proof device (28), which has a **control** part ...
scientific.thomson.com/ts/media/dw/productpdfs/itppdfs/itelcod.pdf - Supplemental Result - Similar pages

[PDF] M68HC11 E SERIES
File Format: PDF/Adobe Acrobat
PPROG enable the programming **voltage, control** the latching of data to be ... Timer

counter value captures and timer **counter** incrementing occur on **opposite** ...
www.mekatronix.com/downloads/docs/hc11_tech_data.pdf - Similar pages

Computer graphics processing, operator interface processing, and ...
In order to prevent a read overhead to cause a disordered image, the analog/digital mixed value **control oscillator** is coupled to an output terminal. ...
www.freshpatents.com/Computer-graphics-processing-operator-interface-processing-and-selective-visual-disp... - 289k - Cached - Similar pages

Result Page: 1 2 3 4 **Next**

Web Results 1 - 4 of 4 for **control counter opposite alternately "n bit" "voltage control" oscillator EMI. (0.**

Tip: Try removing quotes from your search to get more results.

EMI cancellation method and system - Patent 20040096020
In an **EMI** canceller, a **control** signal generation unit includes a **counter**, ... the method comprising: **alternately** outputting an **n-bit** signal and a reverse ...
www.freepatentsonline.com/20040096020.html - 35k - Cached - Similar pages

[PDF] Chip Implementation and Measurement
File Format: PDF/Adobe Acrobat
2.3.3 **Voltage Control Oscillator**. The recovered clock is generated by VCO ... NRZ data is digitally sampled N times producing **N-bit** oversampling data. ...
www.lib.ncsu.edu/theses/available/etd-02202003-151133/unrestricted/etd.pdf - Similar pages

Transmitter circuit, receiver circuit, clock data recovery phase ...
[0054] In one embodiment of the present invention, the present invention includes: a phase comparison loop including a **voltage control** circuit, ...
www.freshpatents.com/Transmitter-circuit-receiver-circuit-clock-data-recovery-phase-locked-loop-circuit-d... - 219k - Supplemental Result - Cached - Similar pages

Switching power converter controller patent invention
[0008] In an alternative embodiment, an **n-bit counter** is used to generate ... [0525] The **voltage control** program (if that is the preferred method of **control** ...
www.freshpatents.com/Switching-power-converter-**control**ler-dt20061214ptan20060279970.php?type=description - 470k - Cached - Similar pages

Basic Search

Advanced Search Search Preferences

control AND counter AND opposite AND alternately AN Search

☑ Journal sources ☑ Preferred Web sources ☑ Other Web sources ☐ Exact phrase

Searched for:: :All of the words:**control** AND **counter** AND **opposite** AND **alternately** AND **"n bit"** AND **"vol**

Found:: :**26 total | 0 journal results | 19 preferred web results | 7 other web results**

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☐ **1.** Untitled Document [PDF-623K]
May 2001
...172 8.8.1 Serial Peripheral **Control** Register...182 9.4.1 Timer **Control** Register 2...192 9.5.5 Timer **Counter** Register . . . . . 193 9.5.6 Timer **Control** Register 1...
[http://mil.ufl.edu/4744/docs/68hc11_tech_data_rev3p1.p...]
similar results

☐ **2.** Untitled Document [PDF-621K]
Dec 2000
...Table of Contents 8.8.1 Serial Peripheral **Control** Register...180 9.4.1 Timer **Control** Register 2...190 9.5.5 Timer **Counter** Register . . . . . 191 9.5.6 Timer **Control** Register 1...
[http://www.phx.devry.edu/datasheets/Digital/68hc11/68h...]
similar results

☐ **3.** Untitled Document [PDF-623K]
Aug 2000
...Table of Contents 8.8.1 Serial Peripheral **Control** Register...180 9.4.1 Timer **Control** Register 2...190 9.5.5 Timer **Counter** Register . . . . . 191 9.5.6 Timer **Control** Register 1...
[http://www.physics.mcmaster.ca/phy4d6/Lab/hc11/hc11er3...]
similar results

☐ **4.** Document: [PDF-862K]
Oct 2002
...**Counters** 21...**Oscillator** Design with HighSpeed CMOS 51 . . . . . RC **Oscillators** 51...Crystal **Oscillators** 51...Injection Current Effect **Control**...
[http://www.eas.asu.edu/~cse521/references/dl129-d.pdf]
similar results

☐ **5.** TECHNICAL DATA iii [PDF-335K]
Aug 1999
...8-5 8.5.1 Serial Peripheral **Control**...9-4 9.2.1 Timer **Control** Register 2...9-9 9.3.5 Timer **Counter** Register...9-9 9.3.6 Timer **Control** Register 1...
[http://www.sonic.net/~mikebr/efi/pdfs/e_series.pdf]

similar results

☐ **6.** Order this document by [PDF-614K]
Oct 2001
...Table of Contents 8.8.1 Serial Peripheral **Control** Register...180 9.4.1 Timer **Control** Register 2...190 9.5.5 Timer **Counter** Register . . . . . .191 9.5.6 Timer **Control** Register 1...
[http://web.tusc.kent.edu/engtech/programs/tech64012/HC...]
similar results

☐ **7.** E:\alevi\dir.text\dir.bindu\PhD Thesis\BinduThesis.pdf [PDF-793K]
Nov 2002
...129 4.2.1 Ring **Oscillator** Design...133 4.2.2 VCO **Control** Stage Design...2.3 Jitter Sources in Ring **Oscillators**...190 4.5 Noise **Control** Mechanisms...
[http://www.usc.edu/dept/engineering/eleceng/Adv_Networ...]
similar results

☐ **8.** Time division multiplier transducer with digitally derived phase shift adjustment for reactive power and energy measurement
Kovalchik, Donald P., Galloway, OH / Battocletti, Frank E., Columbus, OH, *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Oct 1983
...output of crystal **controlled oscillator** 130 may be divided...the output of **oscillator** by 2, a 180...terminals of an **N bit** or position...programmable **counters** 162-164. In...to a crystal **controlled oscillator** represented...
Full text available at patent office. For more in-depth searching go to LexisNexis
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☐ **9.** TIME DIVISION MULTIPLIER TRANSDUCER WITH DIGITALLY DERIVED PHASE SHIFT ADJUSTMENT FOR REACTIVE POWER AND ENERGY MEASUREMENT
KOVALCHIK DONALD P / BATTOCLETTI FRANK E, *PATENT COOPERATION TREATY APPLICATION*, Dec 1982
...shift register, or **alternately**, by a conventional...provided by a crystal **controlled oscillator** or through the utilization...conjunction with voltage **controlled** switch function...poles of a voltage **controlled** switching circuit...labeled signals v2 of **opposite** polarity by virtue...line power and, **alternately**, are passed through...crystal **control oscillator** represented at block...
Full text available at patent office. For more in-depth searching go to LexisNexis
view all 19 results from Patent Offices
similar results

☐ **10.** Suppressing channel-dependent spurious signals in fractional-N synthesizer
Oishi, Kazuaki / Niratsuka, Kimitoshi, *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Nov 2001
...a **voltage-control oscillator** 14 , a frequency...from a quartz **oscillator** (not shown...supplied to the **voltage-control oscillator** 14 . The **voltage-control**...frequency thereof. **Alternately**, the clock...accumulator 16 has a **n-bit** configuration...
Full text available at patent office. For more in-depth searching go to LexisNexis
view all 19 results from Patent Offices
similar results

☐ **11.** Phase synchronisation device and phase quadrature signal generating apparatus
Pickering, Andrew James / Joy, Andrew Keith / Simpson, Susan Mary, *EUROPEAN PATENT APPLICATION*, Apr 1999
...generated by a voltage **controlled oscillator** (VCO) and this output...differential amplifiers with **opposite** polarity and current...voltage **controlled oscillator** or other clock source...from any suitable **oscillatory** source. This potentially...as the output of a **voltage control oscillator**. Rather...
Full text available at patent office. For more in-depth searching go to LexisNexis

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☐ **12.** Phase synchronization
Pickering, Andrew James / Joy, Andrew Keith / Simpson, Susan Mary, *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Jun 2001
...a voltage **controlled oscillator** (VCO) 10...output of a **voltage control oscillator**. Rather...an up/down **counter**. This would...the digital **control** word output...twisted-ring) **counter** type code. This uses an **N-bit** word, PS...
Full text available at patent office. For more in-depth searching go to LexisNexis
view all 19 results from Patent Offices
similar results

☐ **13.** Multiplex control system having enhanced integrity
Floyd, William M., Livonia, MI / Webb, Nathaniel, Detroit, MI / Juzswik, David L., Dearborn Heights, MI, *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Jul 1985
...vehicle for providing **control** to and interacting...terminated at its **opposite** ends or terminals...computer 12 also includes **oscillator** and **counter**/timer circuitry, generally...microprocessor 22, memory 25, **oscillator** and **counter**/timer...typically provide low **voltage control** signals to various...provided by an RC **oscillator** 28 of conventional...
Full text available at patent office. For more in-depth searching go to LexisNexis
view all 19 results from Patent Offices
similar results

☐ **14.** METHOD AND APPARATUS FOR PROVIDING A DIGITAL AUDIO INTERFACE PROTOCOL
BARR, Keith / ZAK, Alan / RYLE, Marcus / BROWN, David / LAFKY, Carl, *EUROPEAN PATENT*, Nov 1994
...receive voltage **controlled oscillator** is affected...applied to the **voltage control** input of the...at the end **opposite** the LED converts...a "coarse" **control**. A **counter** counts the...by a binary **n-bit counter**, where n is...
Full text available at patent office. For more in-depth searching go to LexisNexis
view all 19 results from Patent Offices
similar results

☐ **15.** Method and apparatus for providing a digital audio interface protocol
Barr, Keith / Zak, Alan / Ryle, Marcus / Brown, David / Lafky, Carl, *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Mar 1994
...divided by a binary **n-bit counter**, where n is some...voice data or **control** information, for...include a clock **oscillator** that can be synchronized...synchronize the **oscillator**, (2) start the...voltage, the VCO will **alternately** run at either...frequency of the **oscillator** is constantly changing...
Full text available at patent office. For more in-depth searching go to LexisNexis
view all 19 results from Patent Offices
similar results

☐ **16.** Phase synchronisation
Pickering, Andrew James, Rugby, GB / Joy, Andrew Keith, Great Houghton, GB / Simpson, Susan Mary, Towcester, GB, *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Aug 2000
...a voltage **controlled oscillator** (VCO) 10...output of a **voltage control oscillator**. Rather...an up/down **counter**. This would...the digital **control** word output...twisted-ring) **counter** type code. This uses an **N-bit** word, PS...
Full text available at patent office. For more in-depth searching go to LexisNexis
view all 19 results from Patent Offices
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☐ **17.** Unrestricted frequency changer system and adjustable frequency AC motor drive using such a system

Gyugyi, Laszlo, Penn Hills, PA / Sarkozi, Miklos, Murrysville, PA, *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Dec 1984
...disadvantages. To this effect a **voltage control** method is proposed such that...used in the proposed method of **voltage control**. As shown, the uniform time...output voltage and that the **voltage control** introduces only relatively high...
Full text available at patent office. For more in-depth searching go to LexisNexis
view all 19 results from Patent Offices
similar results

☐ **18.** Improved unrestricted frequency changer system and adjustable frequency AC motor drive using such a system
Gyugyi, Laszlo / Sarkozi, Miklos, *EUROPEAN PATENT*, Dec 1984
...voltage output power to **control** the speed of an AC...motor speeds. A new **voltage control** method is now proposed...Nevertheless, individual **control** of the three output...change. 0 127 306 6. **Control** is simple, that is...prior art method of **voltage control** described in the above...
Full text available at patent office. For more in-depth searching go to LexisNexis
view all 19 results from Patent Offices
similar results

☐ **19.** METHOD AND APPARATUS FOR PROVIDING A DIGITAL AUDIO INTERFACE PROTOCOL
BARR, Keith / ZAK, Alan / RYLE, Marcus / BROWN, David / LAFKY, Carl, *PATENT COOPERATION TREATY APPLICATION*, Jul 1993
...receive voltage **controlled oscillator** is affected...applied to the **voltage control** input of the...at the end **opposite** the LED converts...a "coarse' **control**. A **counter** counts the...by a binary **n-bit counter**, where n is...
Full text available at patent office. For more in-depth searching go to LexisNexis
view all 19 results from Patent Offices
similar results

☐ **20.** Colour television receiver
Sano, Shunichi, Zama, JA / Kasahara, Koichi, Yokohama, JA / Murphy, Jr., Harry C., Apollo, PA / Stauffer, Harry C., Cheswick, PA, *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Jul 1975
...rectangular signals of the **opposite** polarity as shown in FIGS...4 to pass through BBD is **controlled** by the **control** signal whereby...the carrier wave with the **control** signal. One example of a...the voltage **controlled** type **oscillator** VCO. The output from the **oscillator** VCO is applied to a modulator...
Full text available at patent office. For more in-depth searching go to LexisNexis
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Advanced Search Search Preferences

control AND counter AND opposite AND alternately AN | Search

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Searched for:: :All of the words:**control** AND **counter** AND **opposite** AND **alternately** AND **"n bit"** AND **"vol**

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☐ **1.** Document: [PDF-862K]
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